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| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>( Not for submission under 37 CFR 1.99) | Application Number   |                      | 10757772   |  |
|   | Filing Date          |                      | 2004-01-14 |  |
|   | First Named Inventor | Francesco Pappalardo |            |  |
|   | Art Unit             | 2619                 |            |  |
|   | Examiner Name        | Hooman Houshmand     |            |  |
| Attorney Docket Number  |                      | 854063.740           |            |  |

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|   |   |                          |
|---|---|--------------------------|
| 1 | Benini et al., "Architectures and Synthesis Algorithms for Power-Efficient Bus Interfaces," IEEE Transactions on CAD, Vol. 19, No. 9, September 2000, pp. 969-980 .   | <input type="checkbox"/> |
| 2 | Kretzschmar et al., "Adaptive Bus Encoding Technique for Switching Activity Reduced Data Transfer over Wide System Buses," International Workshop - Power and Timing Modelling, Optimization and Simulation (PATMOS2000) Goettingen (D), September 13-15, 2000 (English Abstract Only). | <input type="checkbox"/> |
| 3 | Siegmund et al., "Adaptive Partial Businvert Encoding for Power Efficient Data Transfer Over Wide System Buses," Proceedings on the 13th Symposium on Integrated Circuits and Systems Design, Sept. 18-24 , 2000, pp. 371-376.  | <input type="checkbox"/> |

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| Examiner Signature | /Hooman Houshmand/ | Date Considered | 07/17/2008 |
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